

ISSUE CLASSIFICATION	
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U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>SCANNED <i>CHY</i> Q.A. <i>Am</i></p>	<p>PATENT DATE</p>
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APPLICATION NO.	CONT/PRIOR	CLASS 714	SUBCLASS 733	ART UNIT	EXAMINER 049 NF Decady
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Mark Montierth
Melvin Bodily
Garz Zimmerman

Built-in-self-test using embedded memory and processor in an application specific integrated circuit

TITLE

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		CLAIMS ALLOWED	
			<div>Total Claims</div>	<div>Print Claim for O.G.</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div> <div>_____</div> <div>(Assistant Examiner) (Date)</div> </div>		NOTICE OF ALLOWANCE MAILED	
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	<div> <div>_____</div> <div>(Primary Examiner) (Date)</div> </div>		<div>Amount Due</div>	<div>Date Paid</div>
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